

Bharat Institute of Engineering and Technology

Mangalpally (V), Ibrahimpatnam (M), R.R.Dt -501510

Department of Electronics and Communication Engineering

Time Table for Academic Year 2020-21

Year: III A Semester: II

DAY	I	II		III	IV		V	VI	VII
	9:20-10:10	10:20-11:10	11:10-11:20	11:20-12:10	12:20-1:10		01:50-02:40	02:50-03:40	03:50-04:40
MON	DSP	AI	INTERVAL	AWP	ESD	LUNCH	DSP LAB	APTITUDE TRAINING	DSP LAB
TUE	FOME	DSP		ESD	AWP		VLSI	IN HOUSE TRAINING ON ML/BCT/CS/R P/PYYTHON	DSP
WED	FOME	AI		VLSI	DSP		ESD	TRAINING	VLSI
THUR	FOME	AWP		VLSI	NPTEL/ENS		ECAD LAB	COMM.SKLL TRAINING	ECAD LAB
FRI	AI	AWP		DSP	VLSI(P)		SL LAB	IN HOUSE TRAINING ON ML/BCT/CS/R P/PYYTHON	SL LAB
SAT	ESD(P)	AI		AWP	GATE/ENS		GATE/ENS	TRAINING	
Subject Name				Faculty			CS TRAINING	NRBIETF-ZEHRA	
FUNDAMENTAL OF MANAGEMENT FOR EN				MAMATHA		APTITUDE TRAINING	D.Rajya Laxmi		
ANTENNAS & WAVE PROPAGATION				I.RAVI KUMAR		TRAINING	AI:Dr.ABHISHEK	CS:KIRANMAYI	
VLSI DESIGN				Dr.ANIL KUMAR SAHU		TRAINING	ML:Dr.RAJEEV	PYTHON:Dr.TIRUPAL REDDY	
EMBEDDED SYSTEM DESIGN				T.V.SURESH KUMAR			BC:Dr.SURIA BEGUM	RP:Dr.R.MADAN A MOHANA	
ARTIFICIAL INTELLIGENCE				CH.KRANTHI REKHA					
DIGITAL SIGNAL PROCESSING				Dr.KAMAYANI					
DIGITAL SIGNAL PROCESSING LAB				Dr.KAMAYANI/G.JOY SANGEETH RAJ					
VLSI & ECAD LAB				Dr.PRATEEK KUMAR/T.V.SURESH KUMAR					
SCRIPTING LANGUAGES LAB				CSE FACULTY					

Year: III B Semester: II

DAY	I	II		III	IV	LUNCH	V	VI	VII	
	9:20-10:10	10:20-11:10	11:10-11:20	11:20-12:10	12:20-1:10			01:50-02:40	02:50-03:40	03:50-04:40
MON	AWP	AI	INTERVAL	DSP	ESD			VLSI	APTITUDE TRAINING	
TUE	FOME	AWP		AI	DSP			ECAD LAB	IN HOUSE TRAINING ON ML/BCT/CS/R P/PYYTHON	ECAD LAB
WED	FOME	DSP		VLSI	AI			DSP LAB	TRAINING	DSP LAB
THUR	FOME	AWP		VLSI	NPTEL/ENS			SL LAB	COMM.SKLL TRAINING	SL LAB
FRI	AWP	VLSI		AI(P)	ESD			DSP	IN HOUSE TRAINING ON ML/BCT/CS/R P/PYYTHON	AWP
SAT	VLSI(P)	ESD		DSP	GATE/ENS			GATE/ENS	TRAINING	ESD

Subject Name	Faculty	CS TRAINING	NRBIETF-ANGELIN
FUNDAMENTAL OF MANAGEMENT FOR EN	MAMATHA	APTITUDE TRAINING	D.Rajya Laxmi
ANTENNAS & WAVE PROPAGATION	PRASANTH KULKARNI	TRAINING	AI:Dr.ABHISHEK CS:KIRANMAYI
VLSI DESIGN	N.PITCHESWARA RAO	TRAINING	ML:Dr.RAJEEV PYTHON:Dr.TIRUPAL REDDY
EMBEDDED SYSTEM DESIGN	JOY SANGEETH RAJ		BC:Dr.SURIA BEGUM RP:Dr.R.MADAN A MOHANA
ARTIFICIAL INTELLIGENCE	Dr.RAJEEV		
DIGITAL SIGNAL PROCESSING	Dr.KAMAYANI		
DIGITAL SIGNAL PROCESSING LAB	CH.KRANTHI REKHA/M.K.KHAN		
VLSI & ECAD LAB	T.V.SURESH KUMAR/N.PITCHESWARA RAO		
SCRIPTING LANGUAGES LAB	CSE FACULTY		

Year: III C Semester: II

DAY	I	II		III	IV	LUNCH	V	VI	VII	
	9:20-10:10	10:20-11:10	11:10-11:20	11:20-12:10	12:20-1:10			01:50-02:40	02:50-03:40	03:50-04:40
MON	DSP	ESD	INTERVAL	AWP	AI			SL LAB	APTITUDE TRAINING	SL LAB
TUE	FOME	VLSI		ESD	AWP			DSP LAB	IN HOUSE TRAINING ON ML/BCT/CS/R P/PYYTHON	DSP LAB
WED	FOME	AI		VLSI	ESD			AWP	TRAINING	DSP
THUR	FOME	VLSI		AWP	NPTEL/ENS			DSP	COMM.SKLL TRAINING	
FRI	AI	ESD		VLSI	DSP			ECAD LAB	IN HOUSE TRAINING ON ML/BCT/CS/R P/PYYTHON	ECAD LAB
SAT	AWP(P)	AI (P)		DSP(P)	GATE/ENS			GATE/ENS	TRAINING	VLSI

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FUNDAMENTAL OF MANAGEMENT FOR EN	MAMATHA	APTITUDE TRAINING	D.Rajya Laxmi
ANTENNAS & WAVE PROPAGATION	PRASANTH KULKARNI	TRAINING	AI:Dr.ABHISHEK CS:KIRANMAYI
VLSI DESIGN	Dr.ANILSAHU	TRAINING	ML:Dr.RAJEEV PYTHON:Dr.TIRUPAL REDDY
EMBEDDED SYSTEM DESIGN	JOY SANGEETH RAJ		BC:Dr.SURIA BEGUM RP:Dr.R.MADAN A MOHANA
ARTIFICIAL INTELLIGENCE	Dr.RAJEEV		
DIGITAL SIGNAL PROCESSING	Dr.RITHESH		
DIGITAL SIGNAL PROCESSING LAB	CH.KRANTHI REKHA/KAMAYANI		
VLSI & ECAD LAB	B.PRASANTH/Dr.PRATEEK		
SCRIPTING LANGUAGES LAB	CSE FACULTY		

Year: III D Semester: II

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MON	VLSI	AWP	INTERVAL	AI	DSP			ESD	APTITUDE TRAINING	AWP
TUE	FOME	AI		VLSI	ESD			SL LAB	IN HOUSE TRAINING ON ML/BCT/CS/R P/PYYTHON	SL LAB
WED	FOME	AWP		ESD	DSP			ECAD LAB	TRAINING	ECAD LAB
THUR	FOME	VLSI		DSP	NPTEL/ENS			DSP LAB	COMM.SKLL TRAINING	DSP LAB
FRI	AWP	ESD(P)		AI	VLSI			AI	IN HOUSE TRAINING ON ML/BCT/CS/R P/PYYTHON	DSP
SAT	DSP(P)	AWP(P)		VLSI(P)	GATE/ENS			GATE/ENS	TRAINING	

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ARTIFICIAL INTELLIGENCE	CH.KRANTHI REKHA		
DIGITAL SIGNAL PROCESSING	Dr.RITHESH		
DIGITAL SIGNAL PROCESSING LAB	I.RAVI KUMAR/B.PRASANTH		
VLSI & ECAD LAB	N.PITCHESWARA RAO/G.JOY SANGEETH RAJ		
SCRIPTING LANGUAGES LAB	CSE FACULTY		

TIME TABLE I/C

ACADEMIC I/C

ADMIN I/C